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METHOD FOR CONNECTING AN INTEGRATED CIRCUIT TO
A SUBSTRATE AND CORRESPONDING CIRCUIT
ARRANGEMENT

FIG. 1

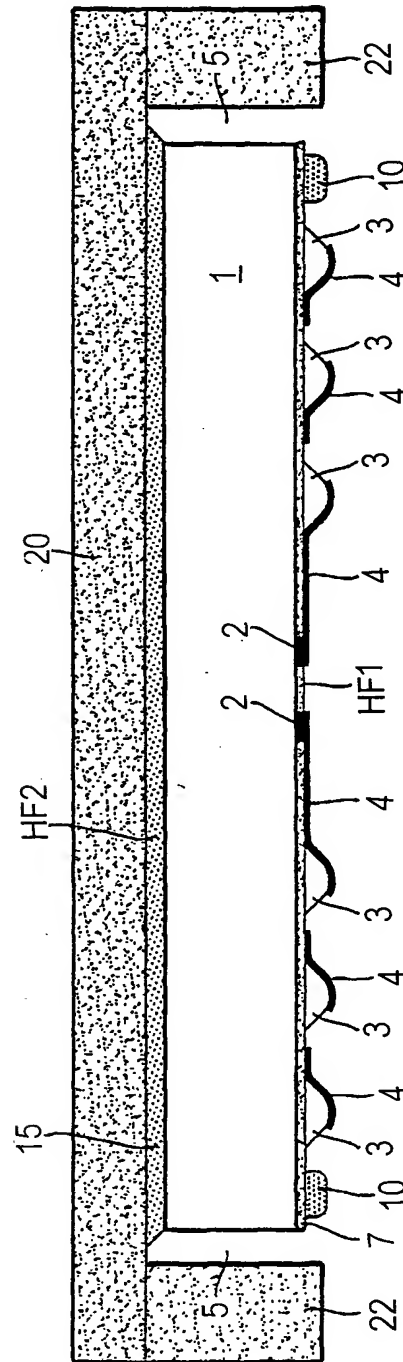


FIG. 2A

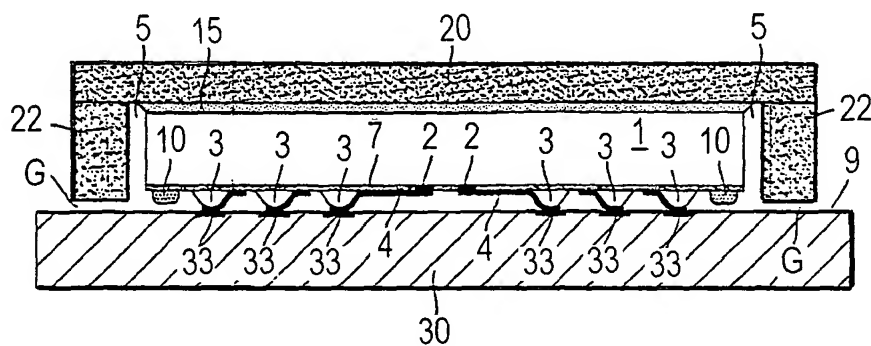


FIG. 2B

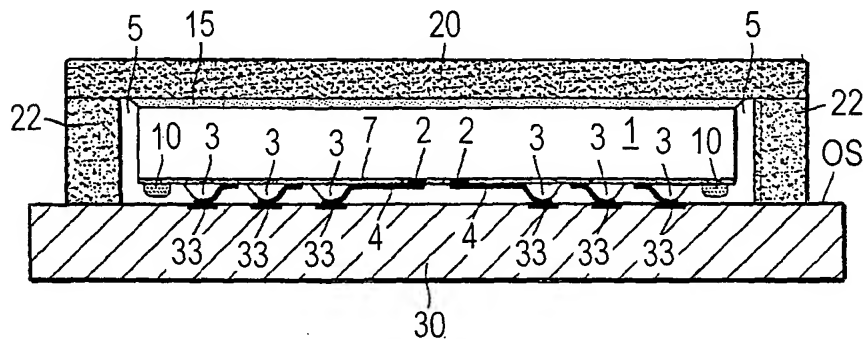


FIG. 2C

